

6252M-PUB

**Wi-Fi Dual-band 2T2R 11ax +Bluetooth 5.2
Combo Module Datasheet**



6252M-PUB Module Datasheet

Office: 6 Floor, Building U6, Junxiang U8 Park,
Hangcheng Avenue, Bao'an District,
Shenzhen City, CHINA

Factory: No.8, Litong Road, Liuyang Economic & Technical
Development Zone, Changsha, Hunan, CHINA

TEL: +86-755-2955-8186

Website: www.fn-link.com

Customer Approval : _____ Company _____

_____ Title _____

_____ Signature _____

_____ Date _____

_____ Fn-Link _____

Revision History

Version	Date	Revision Content	Draft	Approved
1.0	2021/09/26	Initial Release	Tzq	Qjp

CONTENTS

1 Overview.....	1
1.1 Introduction.....	1
1.2 Features.....	1
1.4 General Specification.....	2
1.5 Recommended Operating Rating.....	3
※1.6 EEPROM Information.....	3
2 Wi-Fi RF Specification.....	4
2.1 2.4GHz RF Specification.....	4
2.2 5GHz RF Specification.....	6
3 Bluetooth Specification.....	9
3.1 Bluetooth Specification.....	9
4 Pin Assignments.....	10
4.1 Pin Outline.....	10
4.2 Pin Definition.....	10
5 Dimensions.....	13
5.1 Module Picture.....	13
5.2 Marking Description.....	14
5.3 Connector Specification.....	15
5.3 Module Physical Dimensions.....	15
6 Host Interface Timing Diagram.....	17
6.1 PCIe Bus during Power On Sequence.....	17
6.2 PCIe PERST# Timing Sequence.....	17
6.3 Power Off Sequence.....	18
6.4 BT_DIS Timing Sequence.....	18
6.5 Platform state transitions.....	18
7 Reference Design.....	19
8 Ordering Information.....	20
9 The Key Material List.....	20
10 Recommended Reflow Profile.....	21
11 Package Information.....	22
11.1 Tray.....	22
11.2 Moisture sensitivity.....	22

1 Overview

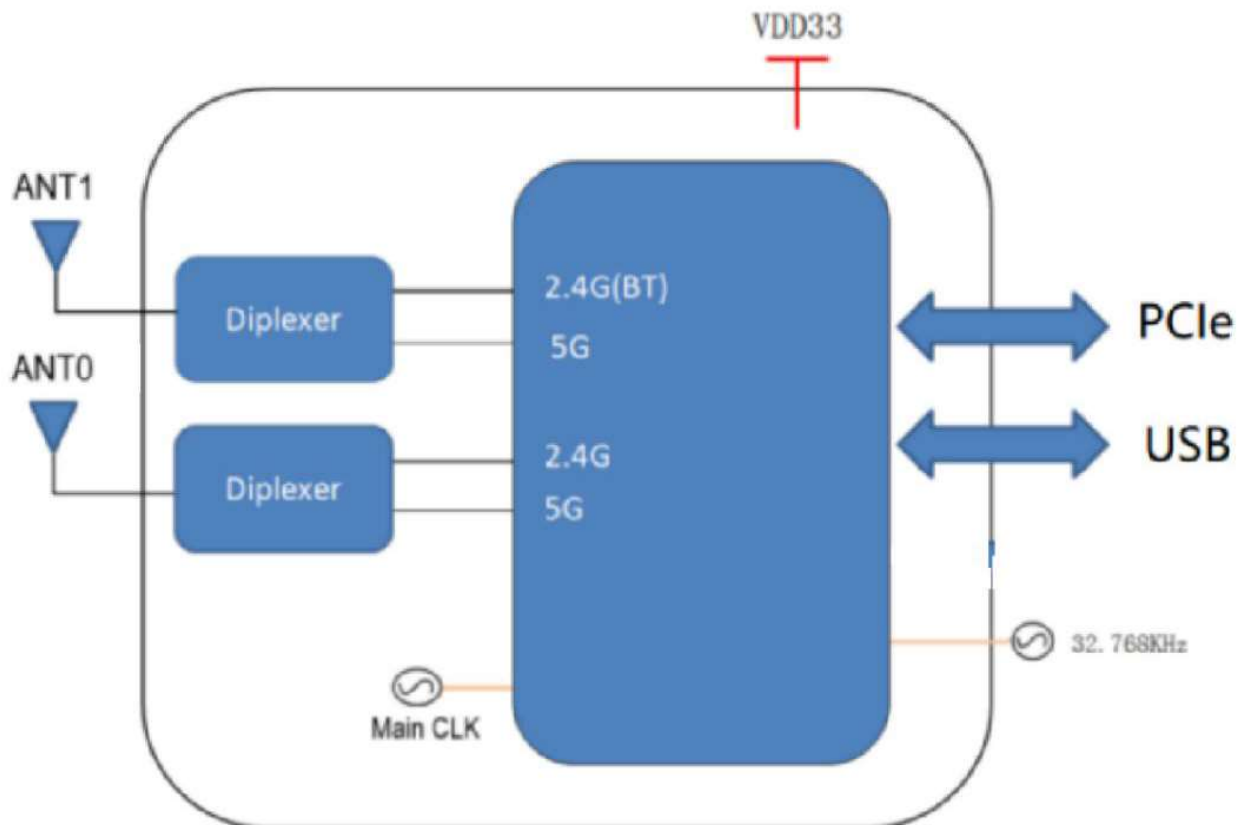
1.1 Introduction

The fn-link6252M-PUB is a highly integrated single-chip that support 2-stream 802. 11ax solutions with Multi-user MIMO (Multiple-Input, Multiple-Output)with Wireless LAN (WLAN) PCI Express network interface controller with integrated Bluetooth 5 USB interface controller.It combines a WLAN MAC, a2T2R capable WLAN baseband, and RF in a single chip. The RTL8852BE provides a complete solution for a high-performance integrated wireless and Bluetooth device.

1.2 Features

- IEEE 802.11a/b/g/n/ac/ax compatible WLAN
- Supports 20/40MHz at 2.4GHz and supports 20/40/80MHz at 5GHz
- Supports 802.11ac 2x2, Wave-2 compliant with RX MU-MIMO
- Complete 802.11n MIMO solution for 2.4GHz and 5GHz band
- Supports low power PCIe(Base Specification Revision 1.1) interface for WLAN and USB(2.0 FS-mode)
- Supports Bluetooth 5.0 system
- Compatible with Bluetooth v2.1+EDR
- Dual Mode support: Simultaneous LE and BR/EDR
- Enhanced BT/Wi-Fi Coexistence Control to improve transmission quality in different profiles
- Supports Bluetooth for class1, class2 and class3 power level transmissions without requiring an external PA
- Integrated 32K oscillator for power management

1.3 Block Diagram



1.4 General Specification

Model Name	6252M-PUB
Product Description	Support Wi-Fi/Bluetooth functionalities
Dimension	L x W x H: 22x 30x 2.2 (typical) mm
Wi-Fi Interface	Support PCIe
BT Interface	USB
Operating temperature	0°C to 70°C
Storage temperature	-55°C to 85°C
RoHS	All hardware components are fully compliant with EU RoHS directive

1.5 Recommended Operating Rating

		Min.	Typ.	Max.	Unit
Operating Temperature		0	25	70	deg.C
VDD33		3.0	3.3	3.6	V
Power Consumption		VDD33 = 3.3V(Unit:mA)			
	Wi-Fi on Mode				
	TX (2.4G 1M)				
	TX (2.4G HT40)				
	RX (2.4G HT40)				
	TX (5G 6M)				
	TX (5G vHT80)				
	RX (5G vHT80)				
	BT on				
	BT Hopping				
	BT TX				
	BT RX				

※1.6 EEPROM Information

Wi-Fi

Vendor ID	
Product ID	

2 Wi-Fi RF Specification

2.1 2.4GHz RF Specification

Feature	Description	
WLAN Standard	IEEE 802.11 b/g/n/ac/ax Wi-Fi compliant	
Frequency Range	2.400 GHz ~ 2.497 GHz (2.4 GHz ISM Band)	
Number of Channels	2.4GHz: Ch1 ~ Ch14	
Spectrum Mask	Complies with IEEE standard	
Freq. Tolerance	±20 ppm	
Test Items	Typical Value	EVM
Output Power ¹	802.11b /11Mbps: 19dBm ± 1.5 dB	EVM ≤ -9dB
	802.11g /54Mbps: 18dBm ± 1.5 dB	EVM ≤ -25dB
	802.11n /MCS7: 17dBm ± 1.5 dB	EVM ≤ -28dB
	802.11ac VHT20 MCS8: 16dBm ± 1.5 dB	EVM ≤ -30dB
	802.11ac VHT40 MCS9: 15dBm ± 1.5 dB	EVM ≤ -32dB
	802.11ax HE20 MCS11:13dBm ± 1.5 dB	EVM ≤ -35dB
	802.11ax HE40 MCS11:13dBm ± 1.5 dB	EVM ≤ -35dB
Test Items	TYP Test Value	Standard Value
SISO Receive Sensitivity (11b,20MHz) @8% PER	- 1Mbps @ -94 dBm	≤-83 dBm
	- 11Mbps @ -85 dBm	≤-76 dBm
SISO Receive Sensitivity (11g,20MHz) @10% PER	- 6Mbps @ -90 dBm	≤-85 dBm
	- 54Mbps @ -71 dBm	≤-68 dBm
SISO Receive Sensitivity (11n,20MHz) @10% PER	- MCS=0 @ -90 dBm	≤-85 dBm
	- MCS=7 @ -69 dBm	≤-67 dBm
SISO Receive Sensitivity (11n ,40MHz) @10% PER	- MCS=0 @ -87 dBm	≤-82 dBm
	- MCS=7 @ -66 dBm	≤-64 dBm
SISO Receive Sensitivity (11ac,20MHz) @10% PER	- MCS=0 @ -90 dBm	≤ -82 dBm
	- MCS=8 @ -64 dBm	≤ -60 dBm
SISO Receive Sensitivity (11ac ,40MHz) @10% PER	- MCS=0 @ -87 dBm	≤ -79 dBm
	- MCS=9 @ -59 dBm	≤ -55 dBm
SISO Receive Sensitivity	- MCS=0 @ -90 dBm	≤-74 dBm

(11ax,20MHz) @10% PER	- MCS=11 @ -60 dBm	≤-52 dBm
SISO Receive Sensitivity	- MCS=0 @ -87 dBm	≤-71 dBm
(11ax ,40MHz) @10% PER	- MCS=11 @ -57 dBm	≤-49 dBm
Maximum Input Level	802.11b: -10 dBm	
	802.11g/n : -20 dBm	
Antenna Reference	Small antennas with 0~2 dBi peak gain	

2.2 5GHz RF Specification

Conditions : VBAT=3.3V ; VDDIO=3.3V ; Temp:25°C

Feature	Description	
WLAN Standard	IEEE 802.11a/n/ac/ax 2x2, Wi-Fi compliant	
Frequency Range	4.900 GHz ~ 5.845 GHz (5.0 GHz ISM Band)	
Number of Channels	5.0GHz: Please see the table ¹	
Test Items	Typical Value	EVM
Output Power ²	802.11a /54Mbps: 18 dBm ± 1.5 dB	EVM ≤ -25dB
	802.11n /MCS7: 17 dBm ± 1.5 dB	EVM ≤ -28dB
	802.11ac VHT20 MCS8: 16 dBm ± 1.5 dB	EVM ≤ -30dB
	802.11ac VHT40 MCS9: 15 dBm ± 1.5 dB	EVM ≤ -32dB
	802.11ac VHT80 MCS9: 15 dBm ± 1.5 dB	EVM ≤ -32dB
	802.11ax HE20 MCS11: 13 dBm ± 1.5 dB	EVM ≤ -35dB
	802.11ax HE40 MCS11: 13 dBm ± 1.5 dB	EVM ≤ -35dB
	802.11ax HE80 MCS11: 13 dBm ± 1.5 dB	EVM ≤ -35dB
Test Items	Test Value	Standard Value
SISO Receive Sensitivity (11a,20MHz) @10% PER	- 6Mbps @ -90 dBm	≤-85
	- 54Mbps @ -71 dBm	≤-68
SISO Receive Sensitivity (11n,20MHz) @10% PER	- MCS=0 @ -90 dBm	≤-85
	- MCS=7 @ -69 dBm	≤-67
SISO Receive Sensitivity (11n,40MHz) @10% PER	- MCS=0 @ -87 dBm	≤-82
	- MCS=7 @ -66 dBm	≤-64
SISO Receive Sensitivity (11ac,20MHz)@10% PER	- MCS=0, NSS1 @ 90 dBm	≤-82
	- MCS=8, NSS1 @ -64 dBm	≤-60
SISO Receive Sensitivity (11ac,40MHz) @10% PER	- MCS=0, NSS1 @ -87 dBm	≤-79
	- MCS=9, NSS1 @ -59 dBm	≤-55
SISO Receive Sensitivity (11ac,80MHz) @10% PER	- MCS=0, NSS1 @ -84 dBm	≤-79
	- MCS=9, NSS1 @ -56 dBm	≤-54
SISO Receive Sensitivity (11ax,20MHz) @10% PER	- MCS=0 @ -90 dBm	≤-74
	- MCS=11 @ -60 dBm	≤-52

SISO Receive Sensitivity (11ax,40MHz) @10% PER	- MCS=0 @ -87 dBm	≤-71
	- MCS=11 @ -57 dBm	≤-49
SISO Receive Sensitivity (11ax,80MHz) @10% PER	- MCS=0 @ -84 dBm	≤-68
	- MCS=11 @ -54 dBm	≤-46
Maximum Input Level	802.11a/n: -30 dBm	
Antenna Reference	Small antennas with 0~2 dBi peak gain	

15GHz(20MHz) Channel table

Band range	Operating Channel Numbers	Channel center frequencies(MHz)
5180MHz~5240MHz	36	5180
	40	5200
	44	5220
	48	5240
5260MHz~5320MHz	52	5260
	56	5280
	60	5300
	64	5320
5550MHz~5700MHz	100	5500
	104	5520
	108	5540
	112	5560
	116	5580
	120	5600
	124	5620
	128	5640
	132	5660
	136	5680
	140	5700
	149	5745
5745MHz~5825MHz	153	5765
	157	5785
	161	5805
	165	5825

3 Bluetooth Specification

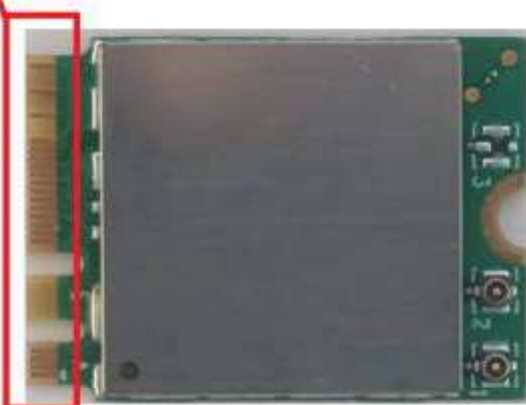
3.1 Bluetooth Specification

Feature	Description		
General Specification			
Bluetooth Standard	Bluetooth V5.2 of 1, 2 and 3 Mbps.		
Host Interface	USB		
Antenna Reference	Small antennas with 0~2 dBi peak gain		
Frequency Band	2402 MHz ~ 2480 MHz		
Number of Channels	79 channels		
Modulation	GFSK, $\pi/4$ -DQPSK,8DPSK		
RF Specification			
	Min.	Typical.	Max.
Output Power		5 dBm	
Sensitivity @ BER=0.1% for GFSK (1Mbps)			-70 dBm
Sensitivity @ BER=0.01% for $\pi/4$ -DQPSK (2Mbps)			-70 dBm
Sensitivity @ BER=0.01% for 8DPSK (3Mbps)			-70 dBm
Maximum Input Level	GFSK (1Mbps):-20dBm		
	$\pi/4$ -DQPSK (2Mbps) :-20dBm		
	8DPSK (3Mbps) :-20dBm		

4 Pin Assignments

4.1 Pin Outline

PIN	Signal	Signal	PIN
74	NC	GND10	75
72	NC	NC	73
70	NC	NC	71
68	NC	GND9	69
66	NC	NC	67
64	NC	NC	65
62	NC	GND	63
60	NC	NC	61
58	NC	NC	59
56	WL_DIS_N	GND	57
54	BT_DIS_N	PEWAKE0	55
52	PERST0	CLKREQ0	53
50	SUSCLK	GND	51
48	COEX_RXD	REFCLKN0	49
46	COEX_TXD	REFCLKP0	47
44	COEX3	GND	45
42	NC	PETN0	43
40	NC	PETP0	41
38	VENDOR_DEFINED	GND	39
36	NC	PERN0	37
34	NC	PERP0	35
32	NC	GND	33
30	NC	NC	31
28	NC	NC	29
26	NC	NC	27
24	NC	NC	25
22	NC	NC	23
20	NC	NC	21
18	GND	NC	19
16	LED_2#	NC	17
14	NC	NC	15
12	NC	NC	13
10	NC	NC	11
8	NC	NC	9
6	LED_1#	GND	7
4	3.3V	USB_D-	5
2	3.3V	USB_D+	3
		GND	1



4.2 Pin Definition

NO	Name	Type	Description	Voltage	
1	GND	-	Ground		
3	USB_D+	I/O	USB differential line for BT		
5	USB_D-	I/O			
7	GND	-	Ground		

9	NC	-			
11	NC				
13	NC				
15	NC				
17	NC	-			
19	NC	-			
21	NC	-			
23	NC	-			
25	NC				
27	NC				
29	NC				
31	NC				
33	GND	-	Ground		
35	PERP0	I	PCIe RX differential signals		
37	PERN0	I			
39	GND	-	Ground		
41	PETP0	O	PCIe TX differential signals		
43	PETN0	O			
45	GND	-	Ground		
47	REFCLKP0	I	PCIe clock differential input signal		
49	REFCLKN0	I			
51	GND		Ground		
53	CLKREQ0	O	PCIe reference clock request signal, open drain, active low	3.3V	
55	PEWAKE0	O	PCIe wake up host, open drain, active low	3.3V	
57	GND	-	Ground		
59	NC	-	NC		
61	NC	-	NC		
63	GND	-	Ground		
65	NC	-	NC		
67	NC	-	NC		
69	GND9	-	Ground		
71	NC	-	NC		

73	NC	-	NC		
75	GND10	-	Ground		

Bottom side

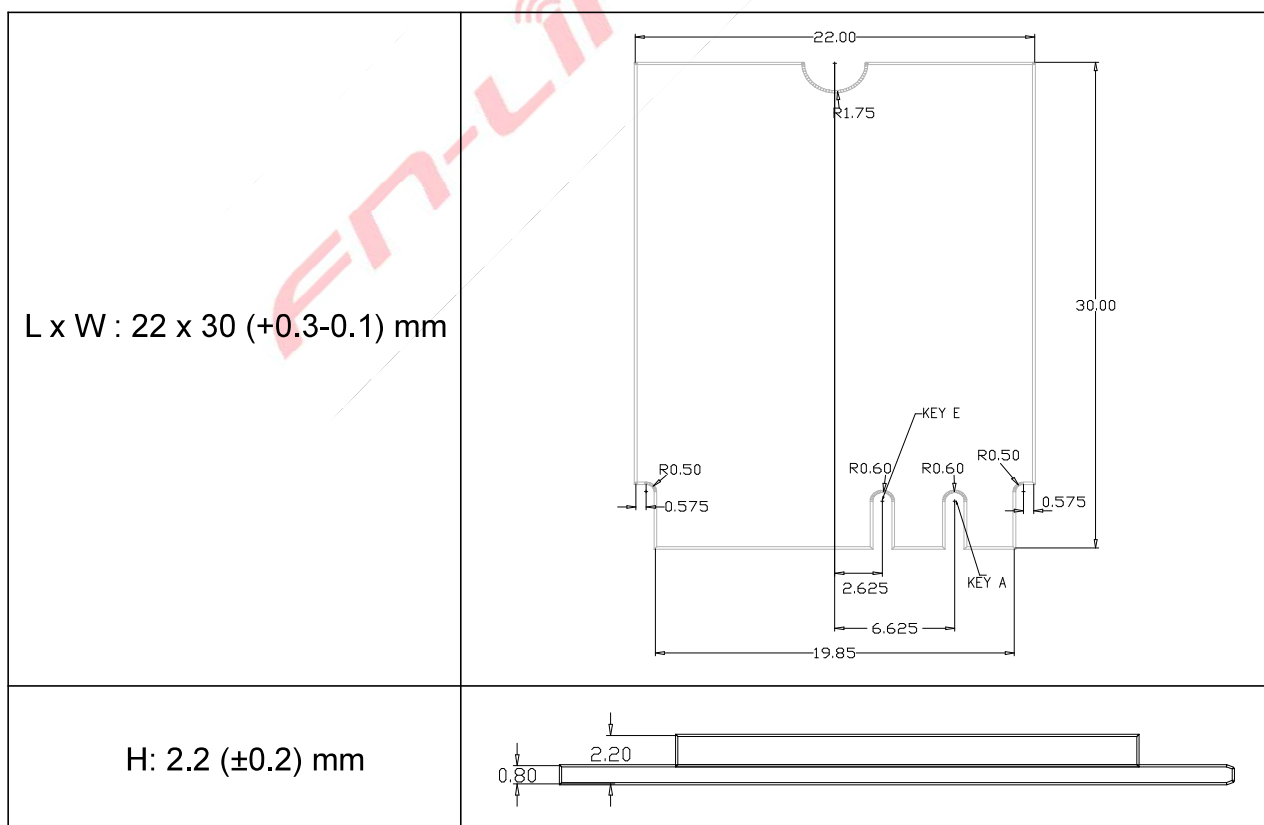
NO	Name	Type	Description	Voltage	
2	3_3V	P	Power supply	3.3V	
4	3_3V	P	Power supply	3.3V	
6	LED_1#	O	WLAN LED signal	3.3V	
8	PCM_CLK	I/O	general perpose input		
10	PCM_SYNC	I/O	general perpose input		
12	PCM_OUT	I/O	general perpose input		
14	PCM_IN	I/O	general perpose input		
16	LED_2#	O	BT LED signal	3.3V	
18	GND	-	Ground		
20	BT_WAKE_HOST_Ant	O	Bluetooth device to wake-up HOST	3.3V	
22	NC	-	NC		
24	NC		NC		
26	NC		NC		
28	NC		NC		
30	NC		NC		
32	NC		NC		
34	NC	-	NC		
36	NC	-	NC		
38	VENDOR DEFINED	-	Host wake BT. No function, please don't connect to this pin.		
40	NC	-	NC		
42	NC	-	NC		
44	COEX3	I/O	LTE coexistence signal	3.3V	
46	COEX_TXD	O	LTE coexistence signal	3.3V	
48	COEX_RXD	I	LTE coexistence signal	3.3V	
50	SUSCLK	I	Sleep clock input	3.3V	
52	PERST0	I	PCIe reset signal, active low	3.3V	

54	BT_DIS_N	I	Bluetooth enable signal, pull low to disable BT function, default high.	3.3V	
56	WL_DIS_N	I	WLAN enable signal, pull low to disable BT function, default high.	3.3V	
58	NC	-	NC		
60	NC	-	NC		
62	NC	-	NC		
64	NC	-	NC		
66	NC	-	NC		
68	NC	-	NC		
70	NC	-	NC		
72	NC	-	NC		
74	NC	-	NC		

P: POWER I: INPUT O: OUTPUT

5 Dimensions

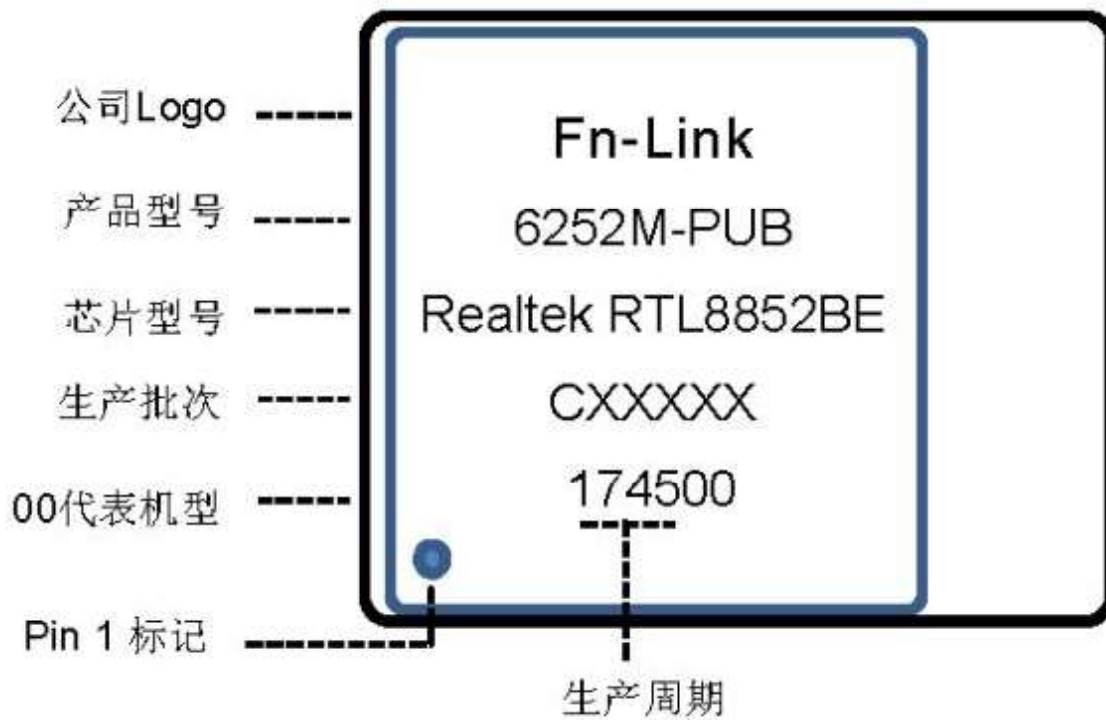
5.1 Module Picture



Weight	2.6g
--------	------

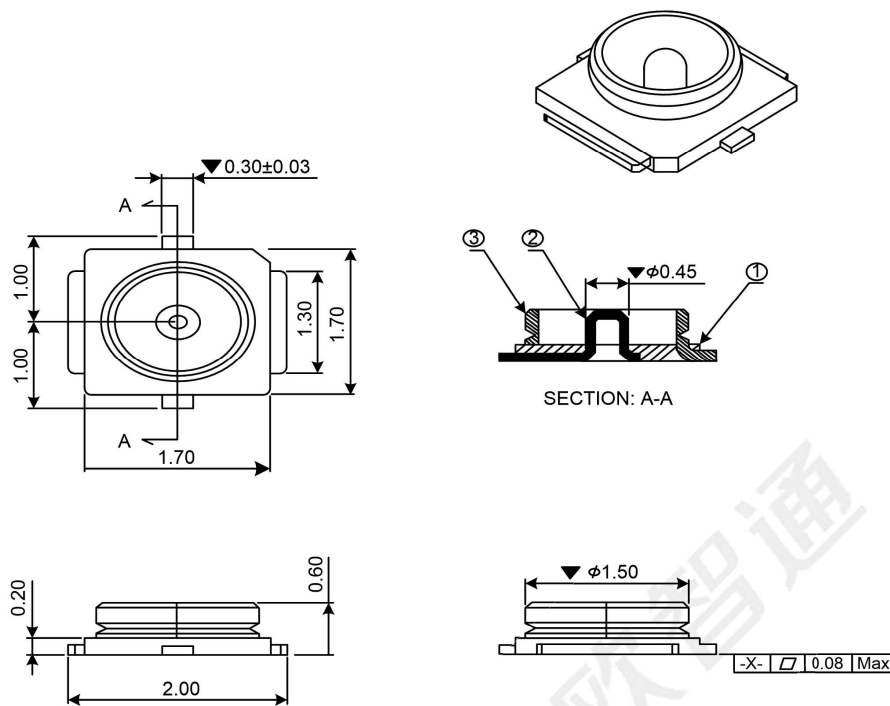
5.2 Marking Description

< TOP VIEW >



模组尺寸: 22x30mm
屏蔽盖尺寸: 21.19x20.98mm

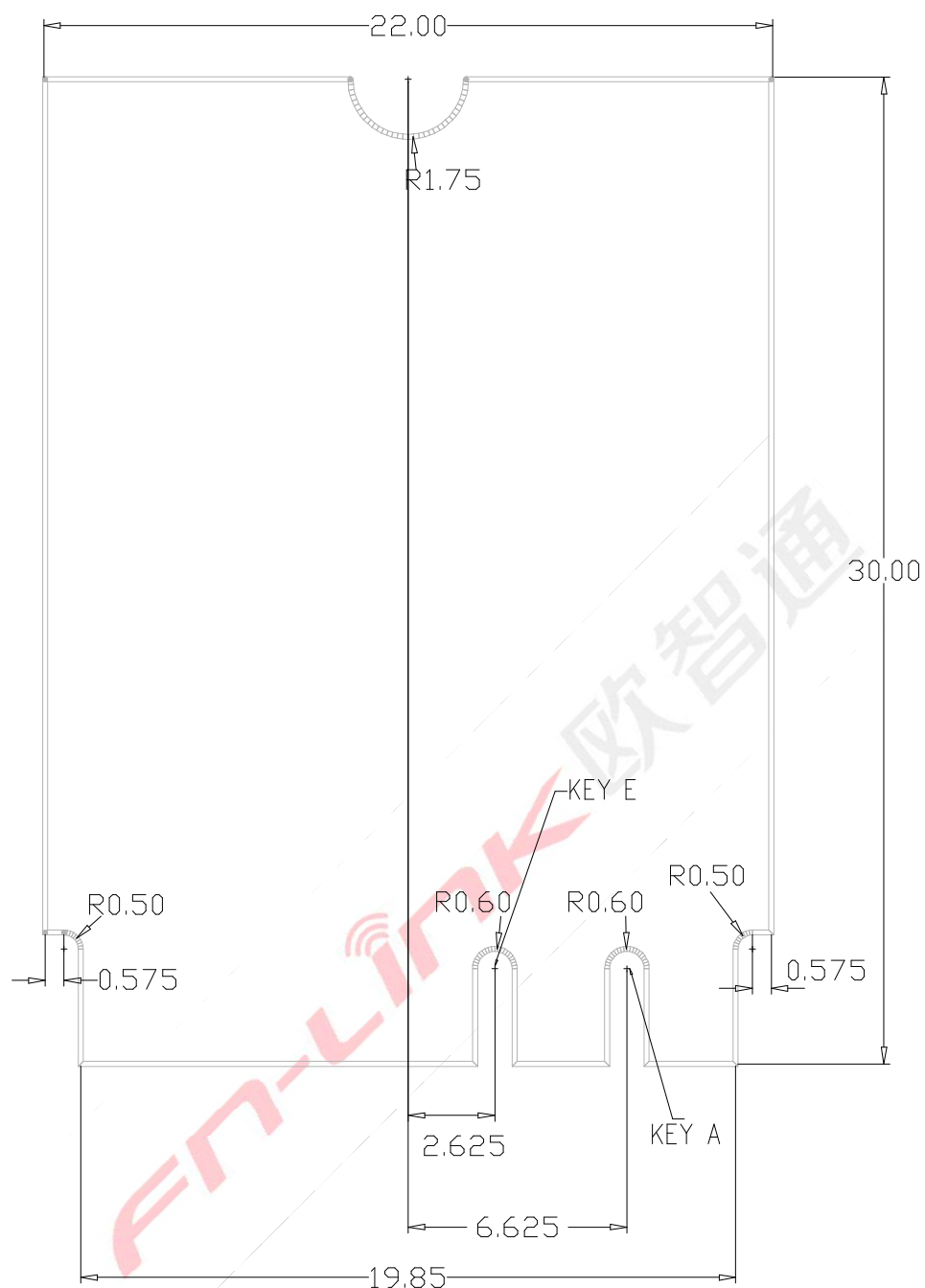
5.3 Connector Specification



5.4 Module Physical Dimensions

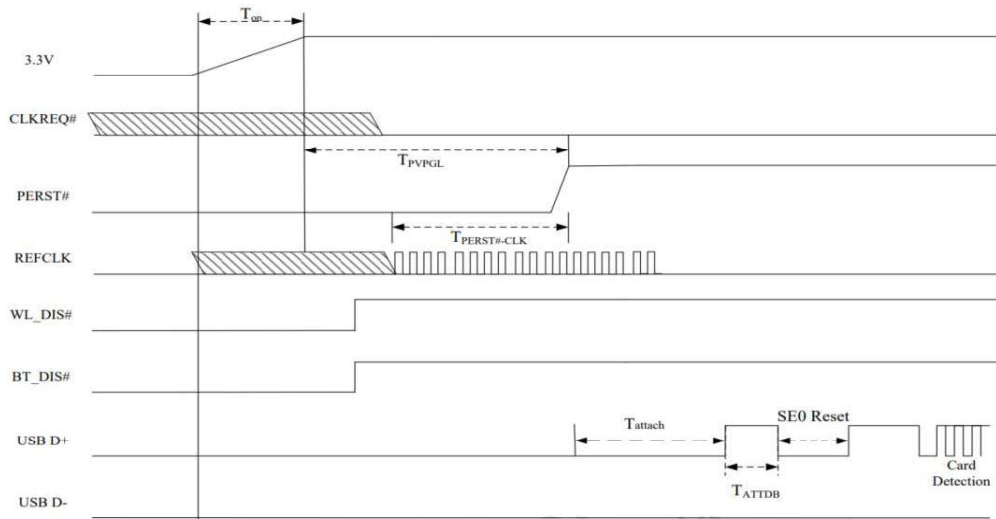
(Unit: mm)

< TOP VIEW >



6 Host Interface Timing Diagram

6.1 PCIe Bus during Power On Sequence



T_{on} : The main power ramp up duration

T_{PVPGL} : Power valid to PERST# input inactive

$T_{PERST\#-CLK}$: Reference clock stable before PERST# inactive

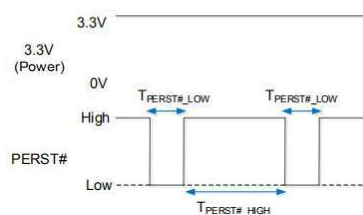
T_{attach} : The interval to turn on BT after PERST# de-asserted

T_{ATTDB} : the debounce interval with a minimal duration of 100ms that provided by the USB system Software

$T_{SE0\ Reset}$: USB host send SE0 Reset duration

Symbol	Unit	Min	Typical	Max
T_{on}	ms	0.5	1.5	5
T_{PVPGL}	ms	Implementation specific; recommended 50ms		
$T_{PERST\#-CLK}$	us	100	--	--
T_{attach}	ms	0.5	2	5
T_{ATTDB}	ms	100	--	--
$T_{SE0\ Reset}$	ms	10	--	--

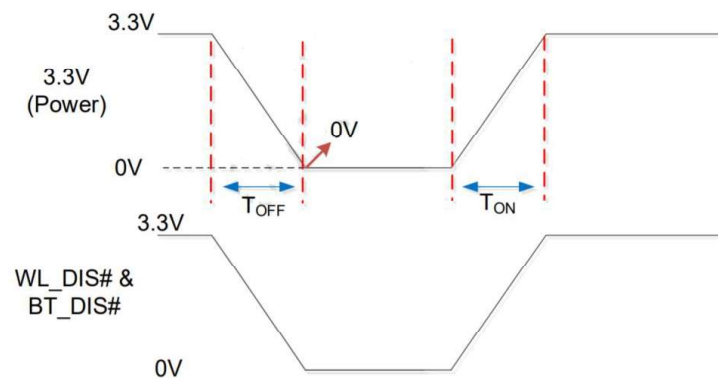
6.2 PCIe PERST# Timing Sequence



RTL8821CE-CG PCIe PERST# Timing Parameters

	Min	Typical	Max	Unit	Description
$T_{PERST\#_LOW}$	6	10	X	ms	PERST# low duration
$T_{PERST\#_HIGH}$	400	500	X	ms	PERST# high duration

6.3 Power Off Sequence

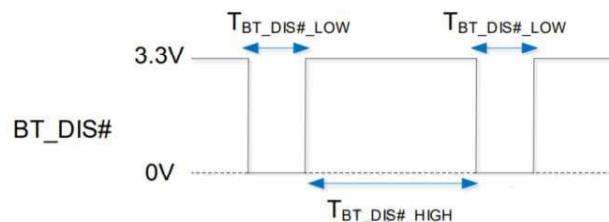


RTL8822CE-CG Power Off Timing Parameters

Symbol	Min	Typical	Max	Unit	Description
T_{OFF}	1.5ms	--	--	ms	Measure point start on 100% Measure point end on 0% (must be 0V)
T_{ON}	0.5	1.5	5	ms	Measure point start on 0% (must be 0V) Measure point end on 100%

Note: If BT_DIS# can't connect to the same power source with 3.3V, it need to be de-asserted before PERST# with 100ms in power on sequence.

6.4 BT_DIS Timing Sequence

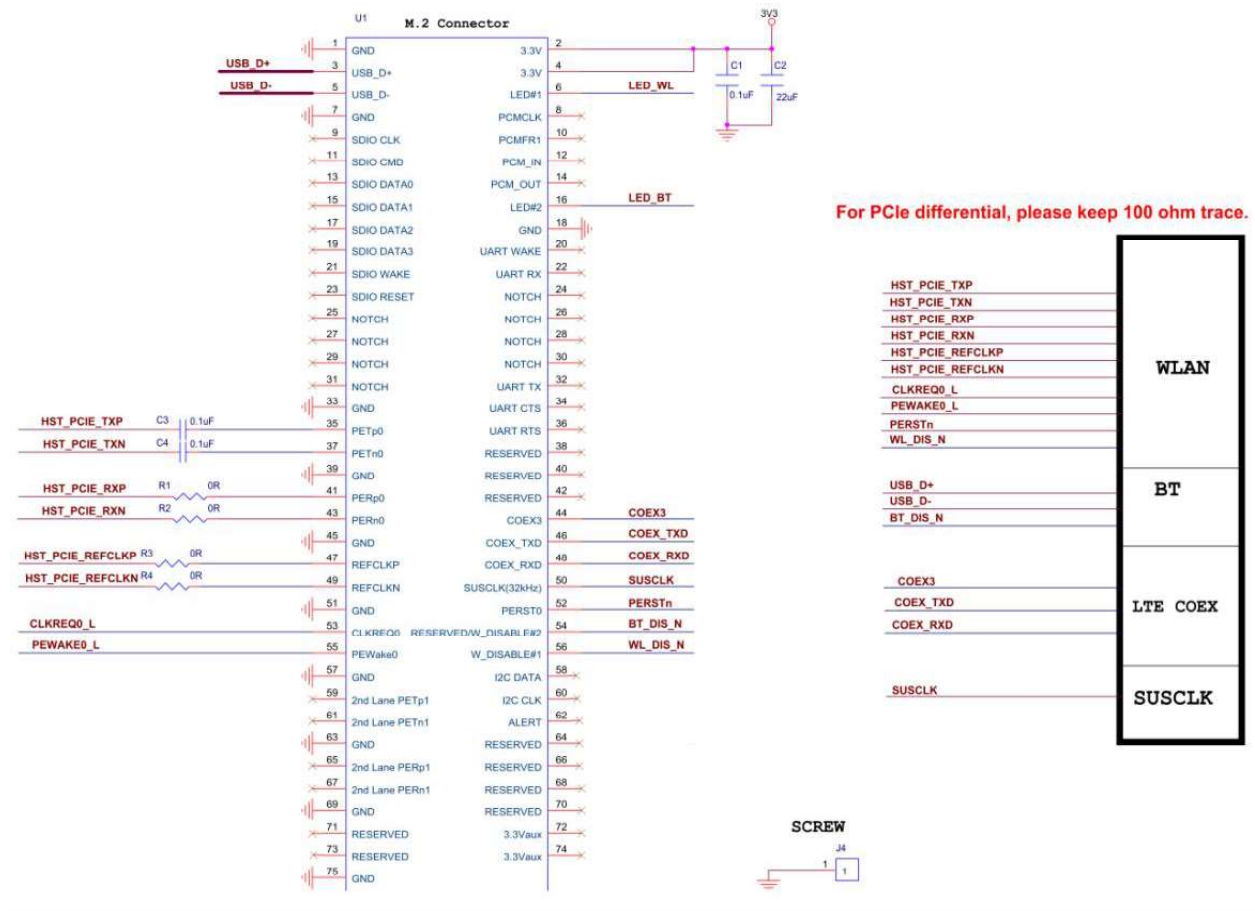


	Min	Typical	Max	Unit	Description
BT_DIS#_LOW	200	--	--	ms	BT_DIS# low duration
BT_DIS#_HIGH	500	--	--	ms	BT_DIS# high duration

6.5 Platform state transitions

3.3V Power range	3.3V Ripple	3.3V Noise	Rise time	
			Min	Max
+/-0.165V	300mVpp @ switching frequency > 100KHz		0.5ms	5ms

7 Reference Design



Note:

- Both of the 2 ANT's are all support 2.4G/5G/BT function.
- 6252M-PUB antenna port is control by driver if diversity function is enabled.
- C1, C2 placed close to module side.
- PCIe differential keep 100 ohm trace.
- USB differential keep 90 ohm trace.

8 Ordering Information

Part No.	Description
FG6252MPUB-00	RTL8852BE-CG, a/b/g/n/ac/ax, Wi-Fi+BT5.2, 2T2R, 22X30mm, PCIE+USB, PCB Version V1.0

9 The Key Material List

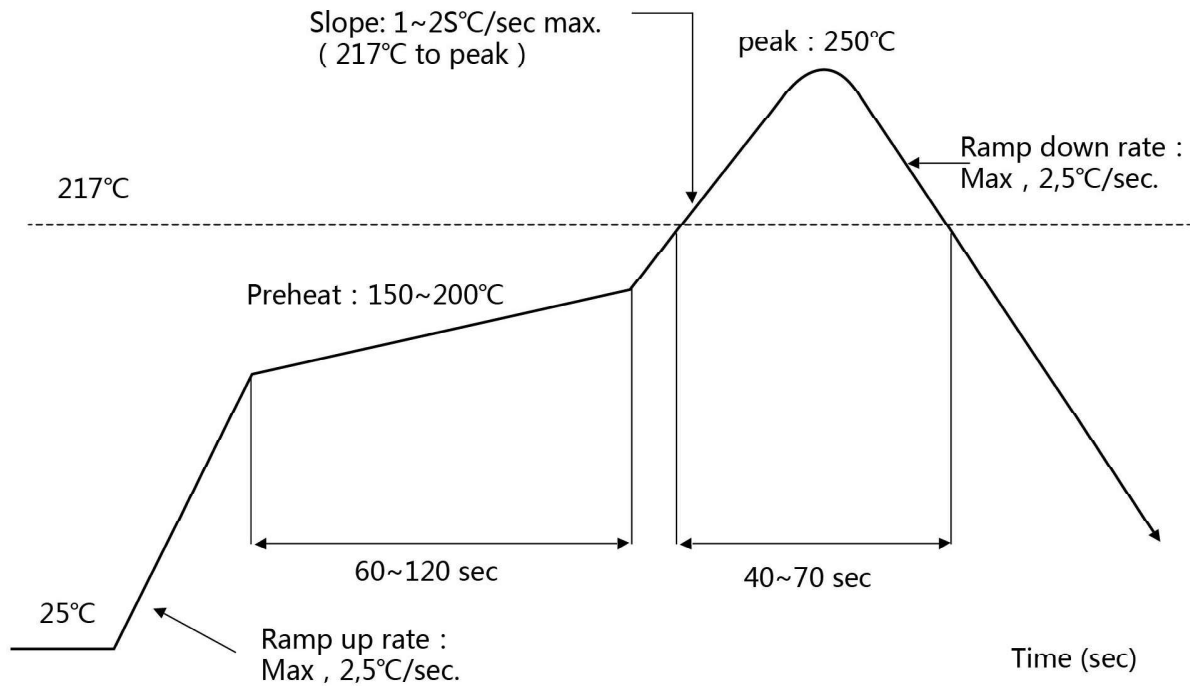
Item	Part Name	Description	Manufacturer
1	Inductor	2520 2.2UH $\pm 20\%$,	Sunlord, Ceaiya, Cenker
2	Diplexer	1608 Dual-band, dual-mode 2.4GHz/5GHz WLAN	Glead, Walsin, ACX, Murata, MAG.LAYERS
3	Crystal	3225 40MHz 12pF $\pm 10\text{ppm}$	ECEC, TKD, Hosonic, JWT, TXC
4	Chipset	RTL8852BE-CG	Realtek
5	PCB	6252M-PUB 22X30X0.8mm TG180	Brain-power, KX-pc, Sunlord, Piotek
6	Shielding Cover	6252M-PUB V1.0 Shielding cover	Suntech, JLitong

10 Recommended Reflow Profile

Referred to IPC/JEDEC standard.

Peak Temperature : $<250^{\circ}\text{C}$

Number of Times : ≤ 2 times



11 Package Information

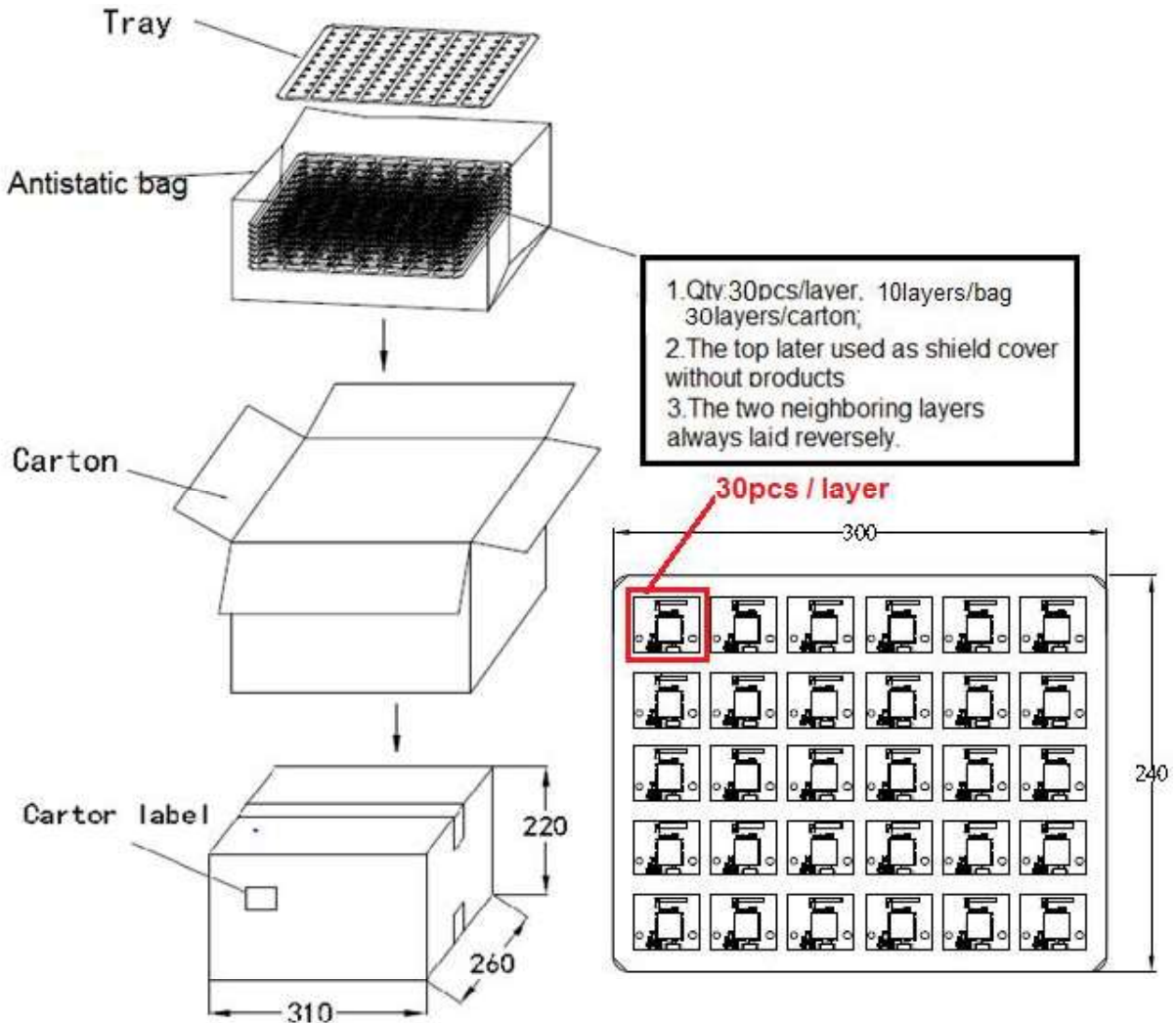
11.1 Tray

Layer size: L300.0*W240.0 mm

Layer material: PVC

Carton size: L310.0*W260.0*H220.0 mm

Carton material: A=A



11.2 Moisture sensitivity

The Modules is a Moisture Sensitive Device level 3, in according with standard IPC/JEDEC J-STD-020, take care

all the relatives requirements for using this kind of components.

Moreover, the customer has to take care of the following conditions:

a) Calculated shelf life in sealed bag: 12 months at <40°C and <90% relative humidity (RH).

- b) Environmental condition during the production: 30°C / 60% RH according to IPC/JEDEC J-STD-033A paragraph 5.
- c) The maximum time between the opening of the sealed bag and the reflow process must be 168 hours if condition
- b) "IPC/JEDEC J-STD-033A paragraph 5.2" is respected
- d) Baking is required if conditions b) or c) are not respected
- e) Baking is required if the humidity indicator inside the bag indicates 10% RH or more